

## PATENT APPLICATION

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of

Docket No: Q75721

Koichi TERASHIMA

Appln. No.: 10/644,812

Group Art Unit: 2815

Confirmation No.: 6181

Examiner: Matthew C. LANDAU

Filed: August 21, 2003

For:

SEMICONDUCTOR DEVICE AND SEMICONDUCTOR DEVICE

MANUFACTURING METHOD

## RESPONSE TO RESTRICTION REQUIREMENT

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

In response to the Office Action dated July 15, 2004 in which the Examiner has required a restriction, Applicant responds by electing Species I, claims 1-15, drawn to a semiconductor device. This election is made without prejudice to the filing of a divisional application directed to the non-elected claims.

Prompt and favorable action on the elected claims are now respectfully requested.

Respectfully submitted.

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Date: August 24, 2004

ard L. Bernstein

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